

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant	: Hideki Hirayama	Art Unit	: 2627
Serial No.	: 10/783,500	Examiner	: Bibbins, Latanya
Filed	: February 20, 2004	Conf. No.	: 7597
Title	: Decoder Having Analog Pll Circuit And Digital Pll Circuit		

MAIL STOP AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT IN REPLY TO NON-FINAL ACTION OF JUNE 22, 2007

Please amend the above-identified application as follows: